

# **Profit Comparison of Computer System with Hardware Redundancy Subject to Different Repair Activities**

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## **Abstract**

In this research paper, main concentrate of the authors on the profit comparison of computer system with hardware redundancy by introducing the concept of priority to software up-gradation, hardware preventive maintenance (PM) and hardware maximum repair time (MRT). The system fails independently from normal mode. All the repair activities such as hardware repair, software up-gradation, hardware preventive maintenance before failure and hardware replacement after maximum repair time are carried out by a single server immediately on need basis. All random variables are statistically independent. The negative exponential distribution is taken for the failure time of the component while the distributions of repair time, up-gradation time, preventive maintenance and replacement time are assumed arbitrary with different probability density functions. Semi-Markov process and regenerative point technique are used. The behaviour of profits of the system models have been examined for different parameters and costs.

**Key Words:** Computer System, Hardware Redundancy, Priority to Software Up-gradation, Repair, Preventive Maintenance, Replacement, Profit Analysis and Stochastic Modelling.

## **1. Introduction**

In current age, computer systems have become an essential part of life, having significant impact on modern society. The importance of computer systems cannot be denied in the corporate or business world, at the workplace and even in one's personnel life. Several techniques have been suggested by the designers and engineers for performance improvement of the systems. The unit wise redundancy technique has been considered as one of these in the development of stochastic models for computer systems. Malik and Anand (2010), Malik and Sureria (2012) and Kumar et al. (2013) analyzed computer systems with cold standby redundancy under different failures and repair policies. Also, Munday et al. (2014, 15, 16) tried to establish a stochastic model for a computer system by providing hardware redundancy in cold standby.

The basic interest of the authors on the profit comparison of computer system with hardware redundancy by introducing the concept of priority to software up-gradation, hardware preventive maintenance (PM) and hardware maximum repair time (MRT). The system fails independently from normal mode. All the repair activities such as hardware repair, software up-gradation, hardware preventive maintenance before failure and hardware replacement after maximum repair time are carried out by a single server immediately on need basis. All random variables are statistically independent. The negative exponential distribution is taken for the failure time of the component while the distributions of repair time, up-gradation time, preventive maintenance and replacement time are assumed arbitrary with different probability density functions. Semi-Markov process and regenerative point technique are used. The behaviour of profits of the system models have been examined for different parameters and costs.

## 2. Notations

$E$	:	Set of regenerative states
$E^-$	:	Set of non-regenerative states
$O$	:	Computer system is operative
$Scs$	:	Software is in cold standby
$PM$	:	Preventive Maintenance
$MRT$	:	Maximum Repair Time
$a/b$	:	Probability that the system has hardware / software failure
$\alpha_0/\beta_0$	:	The rate by which hardware component undergoes for replacement/preventive maintenance
$\lambda_1/\lambda_2$	:	Hardware/Software failure rate
$HFU_r/HFW_r$	:	The hardware is failed and under repair/waiting for repair
$SFU_g/SFWU_g$	:	The software is failed and under/waiting for up-gradation
$HFUR_p/HFWR_p$	:	The hardware is failed and under replacement/waiting for replacement
$HFUP_m/HFWP_m$	:	The hardware is failed and under replacement/waiting for Preventive maintenance
$HFUR/HFWR$	:	The hardware is failed and continuously under repair / waiting for repair from previous state
$SFUG/SFWUG$	:	The software is failed and continuously under up-gradation /waiting for up- gradation from previous state
$HFURP/HFWRP$	:	The hardware is failed and continuously under replacement / waiting for replacement from previous state
$HFUPM/HFPM$	:	The hardware is continuously under/waiting for Preventive maintenance from previous state
$g(t)/G(t)$	:	pdf/cdf of hardware repair time
$f(t)/F(t)$	:	pdf/cdf of software up-gradation time
$r(t)/R(t)$	:	pdf/cdf of hardware replacement time
$m(t)$	:	pdf/cdf of hardware preventive maintenance time
$q_{ij}(t)/Q_{ij}(t)$	:	pdf / cdf of first passage time from regenerative state $S_i$ to a regenerative state $S_j$ or to a failed state $S_j$ without visiting any other regenerative state in $(0, t]$
$q_{ij.k}(t)/Q_{ij.k}(t)$	:	pdf/cdf of direct transition time from regenerative state $S_i$ to a regenerative state $S_j$ or to a failed state $S_j$ visiting state $S_k$ once in $(0, t]$

- $M_i(t)$  : Probability that the system up initially in state  $S_i \in E$  is up at time  $t$  without visiting to any regenerative state
- $W_i(t)$  : Probability that the server is busy in the state  $S_i$  up to time 't' without making any transition to any other regenerative state or returning to the same state via one or more non-regenerative states.
- $\mu_i$  : The mean sojourn time in state  $S_i$  which is given by
- $$\mu_i = E(T) = \int_0^\infty P(T > t) dt = \sum_j m_{ij},$$
- where  $T$  denotes the time to system failure.
- $m_{ij}$  : Contribution to mean sojourn time ( $\mu_i$ ) in state  $S_i$  when system transits directly to state  $S_j$  so that
- $$\mu_i = \sum_j m_{ij} \text{ and } m_{ij} = \int_0^\infty t dQ_{ij}(t) = -q_{ij}'(0)$$
- $\&/\odot$  : Symbol for Laplace-Stieltjes convolution/Laplace convolution
- $*/**$  : Symbol for Laplace Transformation (LT)/Laplace Stieltjes Transformation (LST)
- $P$  : Profit of the Model as shown in Munday et al. (2019)
- $P1$  : Profit of the present model

### 3. System models with Different Repair Activities as shown in following Figures

State Transition Diagram (Basic Model)

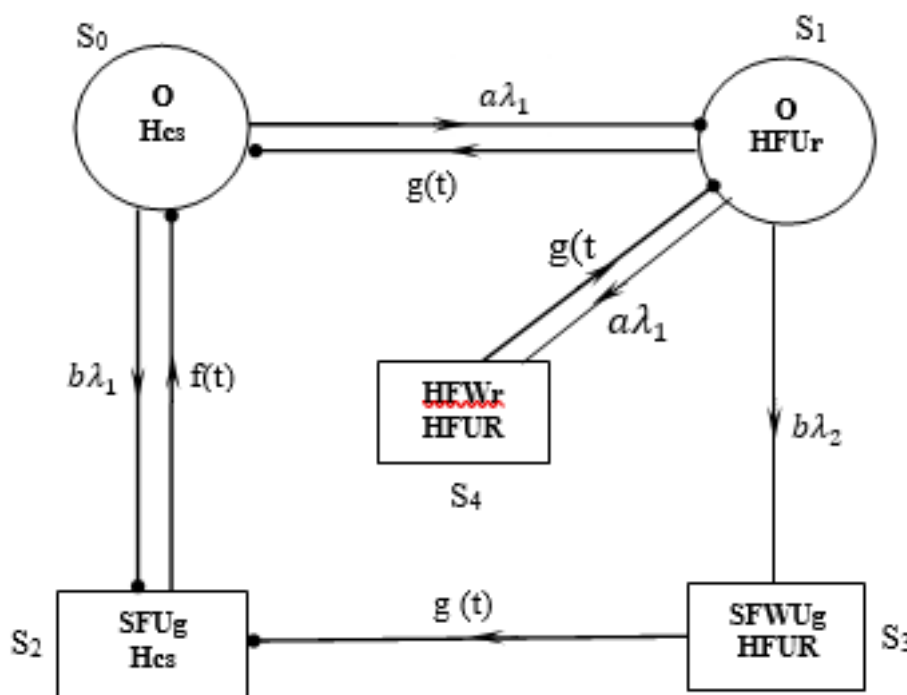


Fig. 1 (Model discussed in research paper [10])

State Transition Diagram (Priority to S/w Up-gradation)

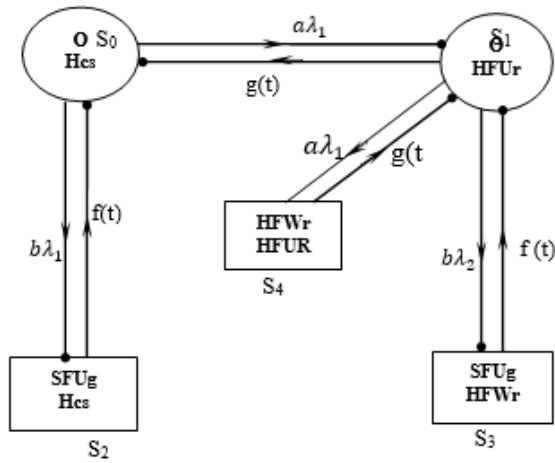


Fig. 2 (Model discussed in research paper [13])

State Transition Diagram (Subject to Maximum Repair Time)

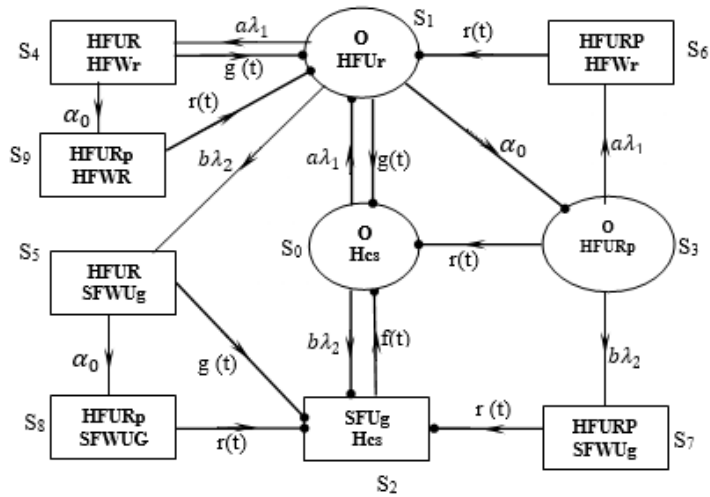


Fig. 3 (Model discussed in research paper [12])

State Transition Diagram (Subject to Preventive Maintenance)

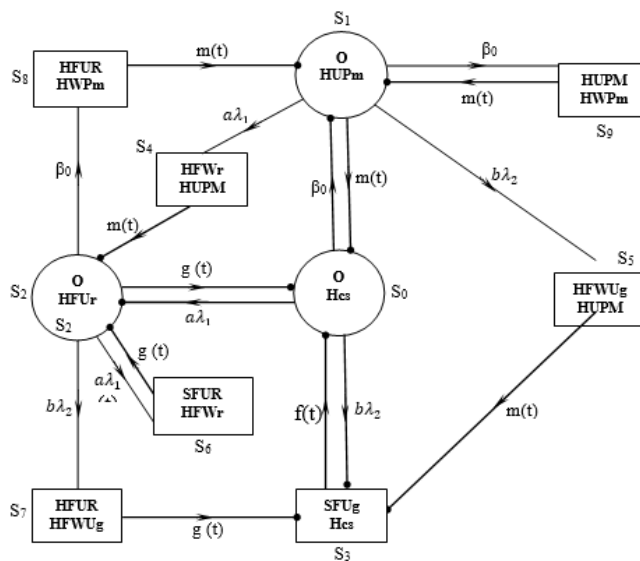


Fig. 4 (Model discussed in research paper [15])

**4. Tabulation of Profit of system model as shown in Fig. 1**

**Table 1: Fig. 1 Profit Vs Hardware Failure Rate ( $\lambda_1$ )**

$\lambda_1$	$\lambda_2=0.001, \alpha=2, \theta=5,$ $a=0.6, b=0.4$	$\lambda_2=0.002$	$\alpha=3$	$\theta=7$	$a=0.4, b=0.6$
0.01	14986.15005	14984.40795	14987.22013	14986.50857	14989.3419
0.02	14973.8227	14972.07423	14976.09294	14974.18092	14981.19235
0.03	14961.28466	14959.52998	14964.88333	14961.64258	14972.94843
0.04	14948.53851	14946.77776	14953.59207	14948.89612	14964.61091
0.05	14935.58681	14933.82014	14942.2199	14935.9441	14956.18055
0.06	14922.43211	14920.65966	14930.7676	14922.78906	14947.6581
0.07	14909.07695	14907.29887	14919.23592	14909.43356	14939.04433
0.08	14895.52387	14893.7403	14907.6256	14895.88012	14930.34
0.09	14881.77539	14879.98647	14895.9374	14882.13127	14921.54585
0.1	14867.83402	14866.0399	14884.17208	14868.18952	14912.66264

**Table 2: Fig. 2 Profit Vs Hardware Failure Rate ( $\lambda_1$ )**

$\lambda_1$	$\lambda_2=0.001, \alpha=2, \theta=5,$ $a=0.6, b=0.4$	$\lambda_2=0.002$	$\alpha=3$	$\theta=7$	$a=0.4, b=0.6$
0.01	14986.17	14984.44	14987.23	14986.53	14989.35501
0.02	14973.88	14972.15	14976.12	14974.23	14981.22646
0.03	14961.39	14959.66	14964.93	14961.75	14973.01136
0.04	14948.72	14946.99	14953.67	14949.07	14964.71044
0.05	14935.85	14934.13	14942.34	14936.21	14956.32442
0.06	14922.8	14921.08	14930.93	14923.16	14947.85399
0.07	14909.57	14907.85	14919.46	14909.93	14939.29988
0.08	14896.16	14894.44	14907.91	14896.51	14930.6628
0.09	14882.56	14880.85	14896.29	14882.92	14921.94344
0.1	14868.79	14867.08	14884.6	14869.15	14913.14252

**Table 3: Fig. 3 Profit Vs Hardware Failure Rate**

$\lambda_1$	$\lambda_2=0.001, \alpha=0.01,$ $\alpha=2,$ $\beta=3, \theta=5, a=0.6,$ $b=0.4$	$\lambda_2=0.002$	$\alpha=0.05$	$\alpha=3$	$\beta=5$	$\theta=7$	$a=0.4,$ $b=0.6$
0.01	14986.87669	14985.142	14985.533	14986.996	14987.692	14987.235	14989.815
1			6	48	22	14	29

0.0 2	14975.40488	14973.668 32	14972.718 54	14975.655 95	14977.171 83	14975.763 05	14982.194 25
0.0 3	14963.84923	14962.110 74	14959.830 01	14964.244 02	14966.701 36	14964.207 13	14974.536 57
0.0 4	14952.20769	14950.467 19	14946.866 09	14952.760 38	14956.276 63	14952.565 31	14966.841 6
0.0 5	14940.4783	14938.735 71	14933.824 9	14941.204 74	14945.893 55	14940.835 63	14959.108 75
0.0 6	14928.65913	14926.914 38	14920.704 66	14929.576 82	14935.548 15	14929.016 17	14951.337 41
0.0 7	14916.74835	14915.001 38	14907.503 63	14917.876 37	14925.236 55	14917.105 1	14943.527
0.0 8	14904.74418	14902.994 94	14894.220 14	14906.103 14	14914.954 96	14905.100 63	14935.676 96
0.0 9	14892.64493	14890.893 36	14880.852 59	14894.256 9	14904.699 69	14893.001 08	14927.786 72
0.1	14880.44896	14878.694 99	14867.399 44	14882.337 43	14894.467 14	14880.804 8	14919.855 76

**Table 4: Fig. 4 Profit Vs Hardware Failure Rate**

$\lambda_1$	$\lambda_2=0.001, \alpha=2, \theta=5,$ $a=0.6,$ $b=0.4, \gamma=0.034,$ $\beta_0=0.001$	$\lambda_2=0.002$	$\alpha=3$	$\theta=7$	$a=0.4,$ $b=0.6$	$\gamma=0.035$	$\beta_0=0.002$
0.0 1	14746.89757	14654.878 77	14749.700 71	14747.057 96	14940.022 86	14918.222 27	13562.176 49
0.0 2	12848.66272	12842.178 38	12851.785 06	12848.808 56	13695.140 39	13051.046 41	10923.332 06
0.0 3	11891.55427	11900.878 43	11895.184 29	11891.695 15	12669.377 14	12110.305 33	9687.6827 37
0.0 4	11272.32349	11286.204 89	11276.546 3	11272.461 54	11998.101 91	11499.773 37	8931.1757 33
0.0 5	10828.51496	10843.715 29	10833.382 88	10828.651 02	11509.353 71	11060.636 57	8410.6084 73
0.0 6	10491.25006	10506.584 37	10496.801 09	10491.384 56	11132.117 64	10725.819 11	8027.1654 59
0.0 7	10224.67343	10239.660 75	10230.938 27	10224.806 66	10829.773 02	10460.407 56	7731.5221 59
0.0 8	10007.79845	10022.241 82	10014.803 62	10007.930 6	10580.846 56	10243.934 32	7495.8567 83

0.0 9	9827.360699	9841.1892 59	9835.1301 47	9827.4919 09	10371.658 55	10063.434 06	7303.1328 25
0.1	9674.502817	9687.7047 14	9683.0587 99	9674.6332 02	10192.974 6	9910.2281 42	7142.2767 52

**Particular Cases**

For  $g(t) = \alpha e^{-\alpha t}$ ,  $f(t) = \theta e^{-\theta t}$ ,  $r(t) = \beta e^{-\beta t}$  and  $m(t) = \gamma e^{-\gamma t}$

**5. Comparative Study of Profit of System Models**

The profit of the basic model has been compared with the profits of other repair activities already discussed in research papers as given in references. It is revealed that the basic model is less profitable as compared to the system models with the concepts of priority to s/w up-gradation and maximum repair time to hardware component but profitable over the concept of preventive maintenance of hardware component. And, hence we can say that the concept of hardware preventive maintenance in a computer system with hardware redundancy in cold standby is not much helpful in making the system more profitable. The graphical presentation of profits of the system models with respect to hardware failure rate ( $\lambda 1$ ) have been shown numerically in tables 1 to 4. Finally, it is concluded that a computer system can be made more reliable and profitable to use by providing hardware redundancy in cold standby and maximum hardware repair time to the server.

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